SPECIFICATIONS FOR
LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	**AM-800600MPTNQW-T20H**
APPROVED BY	
DATE	

☐ **Preliminary Specification**
■ **Formal Specification**

Approved by	Checked by	Organized by
Patrick	Jessica	Mantle

This Specification is subject to change without notice.

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RECORD OF REVISION

Revision Date	Page	Contents	Editor
2021/08/09	--	New Release	Mantle

1. INSTRUCTION

Ampire 8.4” Display Module is a color active matrix TFT-LCD that uses amorphous silicon TFT as a switching device . This model is composed of a TFT-LCD panel, a driving circuit. This TFT-LCD has a high resolution (800(R.G.B) X 600) and can display up to 262,144 colors.

1.1 Features

(1) Construction : a-Si TFT-LCD with driving system, White LED Backlight.

(2) LCD type : Transmissive , Normally White

(3) Number of the Colors : (a) 262K colors (LVDS 6 bits mode) (default)

(b) 16.2M colors (LVDS 8 bits mode).

(4) LVDS Interface **(Default setting : 6 bit mode)**.

(5) LCD Power Supply Voltage : 3.3V single power input, built-in power supply circuit.

(6) Build-in New LED driver IC: AL3065 (VLED=12V).

(7) ROHS compliant.

(8) Capacitive touch panel: Black frame /I2C/ILI 2511

2. PHYSICAL SPECIFICATIONS

Item	Specifications	unit
Display resolution(dot)	800RGB (W) x 600(H)	dots
Active area	170.40 (W) x 127.80(H)	mm
Pixel pitch	213 (W) x 213 (H)	um
Color configuration	R.G.B -stripe	
Overall dimension	212.17(W) x 166.8(H) x 10.875(D)	mm
Weight	T.B.D	g
Backlight unit	LED	
Display color	262K (default)	colors
Driver IC	source IC: ILI6123H-9G Gate IC: ILI5600	

If user wants to change the default setting for mass production, please contact with Ampire. We'll apply a new P/N for you.

3. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Note
Supply voltage range	VCC	-0.3	4	V	(1)
Voltage range at any terminal	VI	-0.3	VCC + 0.3	V	
Operating Temperature	Top	-20	70	℃	
Storage Temperature	Tstg	-30	80	℃	

Note : All voltage values are with respect to the GND terminals unless otherwise noted.

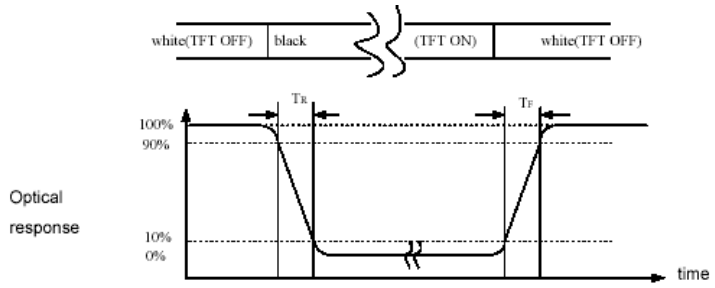
4. OPTICAL CHARACTERISTICS

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Response Time		$T_r + T_f$	Θ=Φ=0°	-	8	16	ms	(1)
Contrast ratio		CR		480	600	-	-	(2)(3)
Viewing Angle	Horizontal	ΘL	CR≧10	65	75	-	Deg.	(5)
		ΘR		65	75	-		
	Vertical	ΘU		50	60	-		
		ΘD		60	70	-		
Luminance (Center)		L	Θ=Φ=0°	340	425	--	cd/m^2	(3)(4) IL=80mA Ta=25℃
Luminance Uniformity		ΔL		-	70	-	%	(3)(4)
Color chromaticity	White	Wx		0.26	0.31	0.367		
		Wy		0.28	0.33	0.38		

NOTE :

- These items are measured by BM-5A(TOPCON) or CA-1000(MINOLTA) in the dark room (no ambient light)

(1) Definition of Response Time (White-Black)



(2) Definition of Contrast Ratio

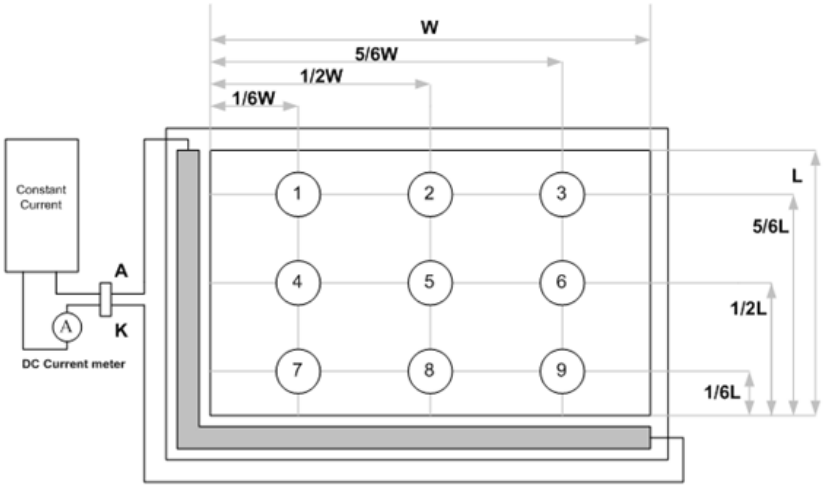
Measure contrast ratio on the below 5 points(refer to figurel,#1~#5point) and take the average value

Contrast ratio is calculated with the following formula :

Contrast Ratio(CR)=(White)Luminance of ON ÷ (Black)Luminance of OFF

(3) Definition of Luminance:

Measure the luminance of white state at center point.

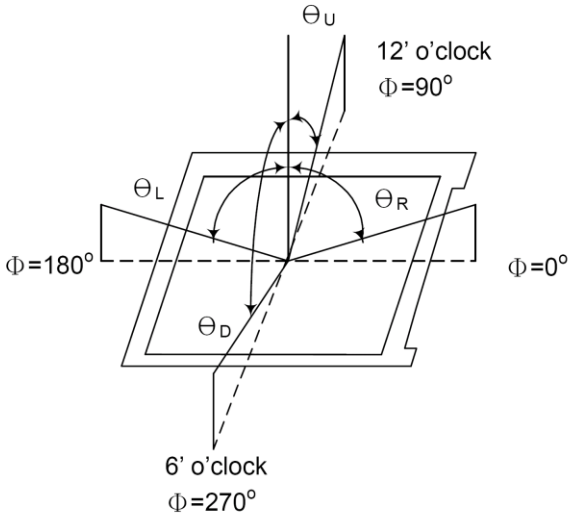


(4)Definition of Luminance Uniformity :
Measured Maximum luminance[L(MAX)] and Minimum luminance[L(MIN)] on the 9 points

Luminance Uniformity is calculated with the following formula :

ΔL =[L(MIN) / L (MAX)] X 100%

(5)Definition of Viewing Angle



5. ELECTRICAL CHARACTERISTICS

5.1 Power Specification

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Logic / LCD Drive Voltage	VCC	3.0	3.3	3.6	V	
VCC Current	ICC		120		mA	(1)

Note1: fv =60Hz , Ta=25℃ , Display pattern : All Black

5.2 LVDS electrical Specification

Vcc = 3.0 - 3.6V, Ta = -10 - +70 ˚C

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
CMOS/TTL DC SPECIFICATIONS						
V_{IH}	High Level Input Voltage		2.0		Vcc	V
V_{IL}	Low Level Input Voltage		GND		0.8	V
V_{OH}	High Level output Voltage	I_{OH}=-4mA	2.4			V
V_{OL}	Low Level Output Voltage	I_{OL}=4mA			0.4	V
I_{IN}	Input Current	0V ≦ V_{IN} ≦ Vcc			±10	μA
I_{PD}	Pull Down Current	R/F pin,V_{IH}=Vcc			100	μA
I_{OS}	Output Short Circuit Current	V_{OUT}=0V			-50	mA
LVDS DRIVER DC SPECIFICATIONS						
V_{OD}	Differential Output Voltage	RL=100Ω	250	350	450	mV
ΔV_{OD}	Change in VOD between Complimentary Output States				35	mV
V_{OC}	Common Mode Voltage		1.125	1.25	1.375	V
ΔV_{OC}	Change in VOC between Complimentary Output States				35	mV
I_{OS}	Output Short Circuit Current	V_{OUT}=0V,RL=100Ω			-24	mA
I_{OZ}	Output TRI-STATE Current	/PDWN=0V, V_{OUT}=0V to Vcc			±10	μA
LVDS RECEIVER DC SPECIFICATIONS						
V_{TH}	Differential Input High Threshold	V_{OC}=+1.2V			+100	mV
V_{TL}	Differential Input low Threshold		-100			mV
I_{IN}	Input Current	V_{IN}=+2.4V/ 0V Vcc=3.6V			±10	μA

6. BACKLIGHT UNIT

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Input Voltage	VLED	10.8	12.0	12.6	V	
Input Current	ILED	--	300	--	mA	100% PWM duty
Dimming Frequency	Fpwm	100		25K	Hz	
Dimming Voltage High	V_{DIM_H}	2.5	--	5	V	
Dimming Voltage Low	V_{DIM_L}	--	--	0.3	V	
Enable Voltage High	V_{EN_H}	2.4	--	5.0	V	
Enable Voltage Low	V_{EN_L}	0	--	0.5	V	
LED Forward Current	IF	--	80	--	mA	Ta=25℃
LED Forward Voltage	VF		38.4	43.8	V	IF=80mA, Ta=25℃
LED life time			30,000	-	Hr	IF=80mA, Ta=25℃

Note 1: Ta means ambient temperature of TFT-LCD module.

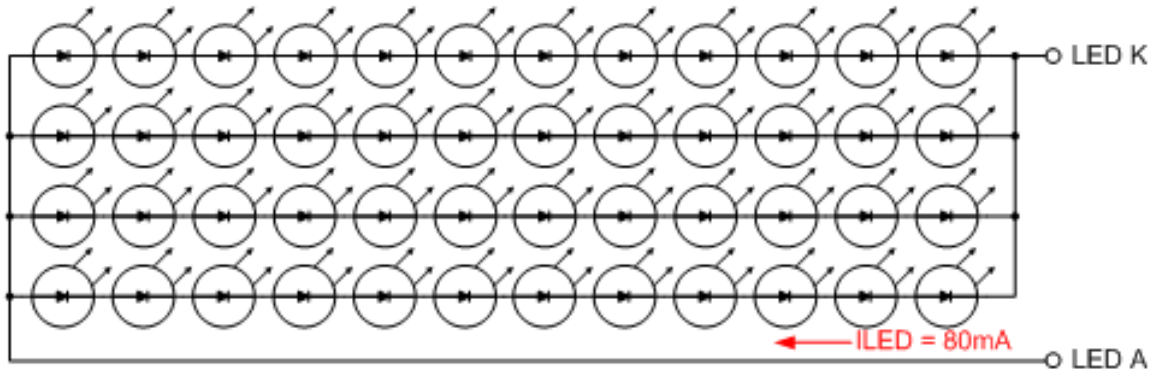
Note 2: VLED, ILED are defined for LED B/L. (100% duty of PWM dimming)

Note 3: IF, VF, Fpwm are defined for LED Driver.

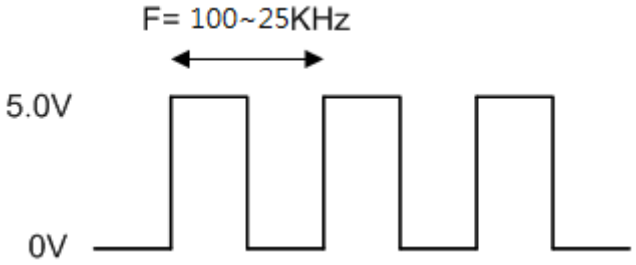
Note 4: If the module is driven by high current or at high ambient temperature & humidity condition. The operating life will be reduced.

Note 5: Operating life means brightness goes down to 50% minimum brightness. LED life time is estimated data.

Note 6: the structure of LED B/L shows as below.



6.1 PWM Dimming Control



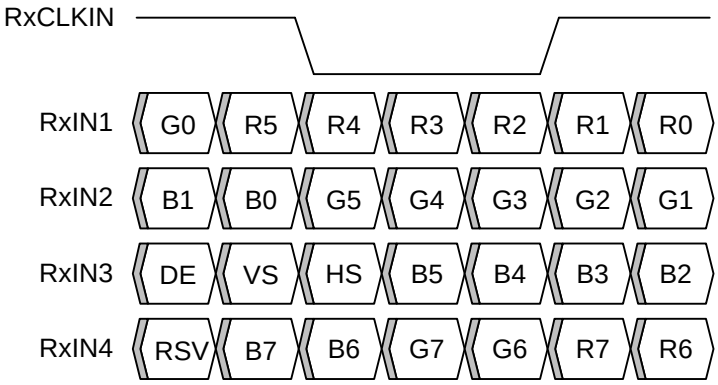
7. INTERFACE

7.1 Interface Definition

CN1 : LVDS Connector

Pin no	Symbol	Function
1	VCC	POWER SUPPLY:3.3V
2	VCC	POWER SUPPLY:3.3V
3	UD	Vertical Reverse Scan Control.
4	LR	Horizontal Reverse Scan Control.
5	RXIN1-	Transmission Data of Pixels 1
6	RXIN1+	Transmission Data of Pixels 1
7	GND	Power Ground
8	RXIN2-	Transmission Data of Pixels 2
9	RXIN2+	Transmission Data of Pixels 2
10	GND	Power Ground
11	RXIN3-	Transmission Data of Pixels 3
12	RXIN3+	Transmission Data of Pixels 3
13	GND	Power Ground
14	RXCKIN-	Sampling Clock
15	RXCKIN+	Sampling Clock
16	GND	Power Ground
17	NC	No connection
18	NC	No connection
19	NC	**LVDS 6 bits mode : No connection (default)** LVDS 8 bit mode : (RXIN4-) Transmission Data of Pixels 4
20	NC	**LVDS 6 bits mode : No connection (default)** LVDS 8 bit mode : (RXIN4+) Transmission Data of Pixels 4

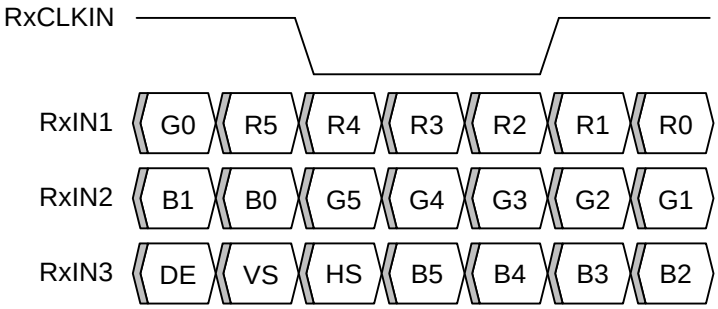
8 bits LVDS input



Note : R/G/B data 7 : MSB, R/G/B data 0 : LSB

Signal Name	Description	Remark
R7 R6 R5 R4 R3 R2 R1 R0	Red Data 7 (MSB) Red Data 6 Red Data 5 Red Data 4 Red Data 3 Red Data 2 Red Data 1 Red Data 0 (LSB)	Red-pixel Data Each red pixel's brightness data consists of these 8 bits pixel data.
G7 G6 G5 G4 G3 G2 G1 G0	Green Date 7 (MSB) Green Date 6 Green Date 5 Green Date 4 Green Date 3 Green Date 2 Green Date 1 Green Date 0 (LSB)	Green-pixel Data Each green pixel's brightness data consists of these 8 bits pixel data.
B7 B6 B5 B4 B3 B2 B1 B0	Blue Data 7 (MSB) Blue Data 6 Blue Data 5 Blue Data 4 Blue Data 3 Blue Data 2 Blue Data 1 Blue Data 0 (LSB)	Blue-pixel Data Each blue pixel's brightness data consists of these 8 bits pixel data.
RxCLKIN+ RxCLKIN-	LVDS Clock Input	
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	

6 bits LVDS input

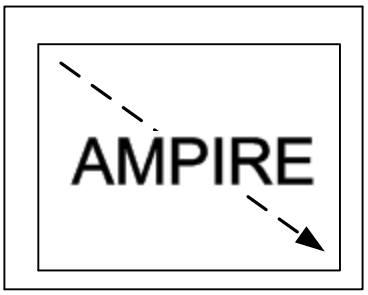


Note : R/G/B data 5 : MSB, R/G/B data 0 : LSB

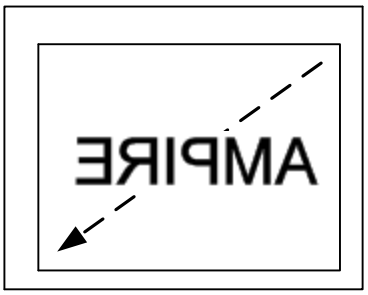
Signal Name	Description	Remark
R5 R4 R3 R2 R1 R0	Red Data 5 (MSB) Red Data 4 Red Data 3 Red Data 2 Red Data 1 Red Data 0 (LSB)	Red-pixel Data Each red pixel's brightness data consists of these 6 bits pixel data.
G5 G4 G3 G2 G1 G0	Green Date 5 (MSB) Green Date 4 Green Date 3 Green Date 2 Green Date 1 Green Date 0 (LSB)	Green-pixel Data Each green pixel's brightness data consists of these 6 bits pixel data.
B5 B4 B3 B2 B1 B0	Blue Data 5 (MSB) Blue Data 4 Blue Data 3 Blue Data 2 Blue Data 1 Blue Data 0 (LSB)	Blue-pixel Data Each blue pixel's brightness data consists of these 6 bits pixel data.
RxCLKIN+ RxCLKIN-	LVDS Clock Input	
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	

Setting of scan control input		Scanning direction
UD	**LR**	
GND	GND	Up to Down, Left to Right
VCC	VCC	Down to Up, Right to Left
GND	VCC	Up to Down, Right to Left
VCC	GND	Down to Up, Left to Right

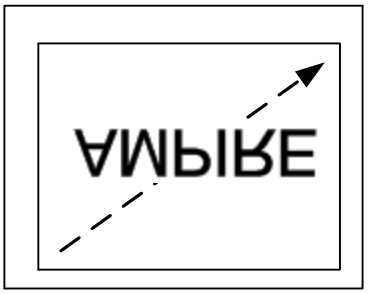
UD : L, LR : L



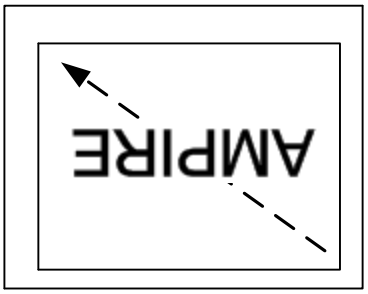
UD : L, LR : H



UD : H, LR : L



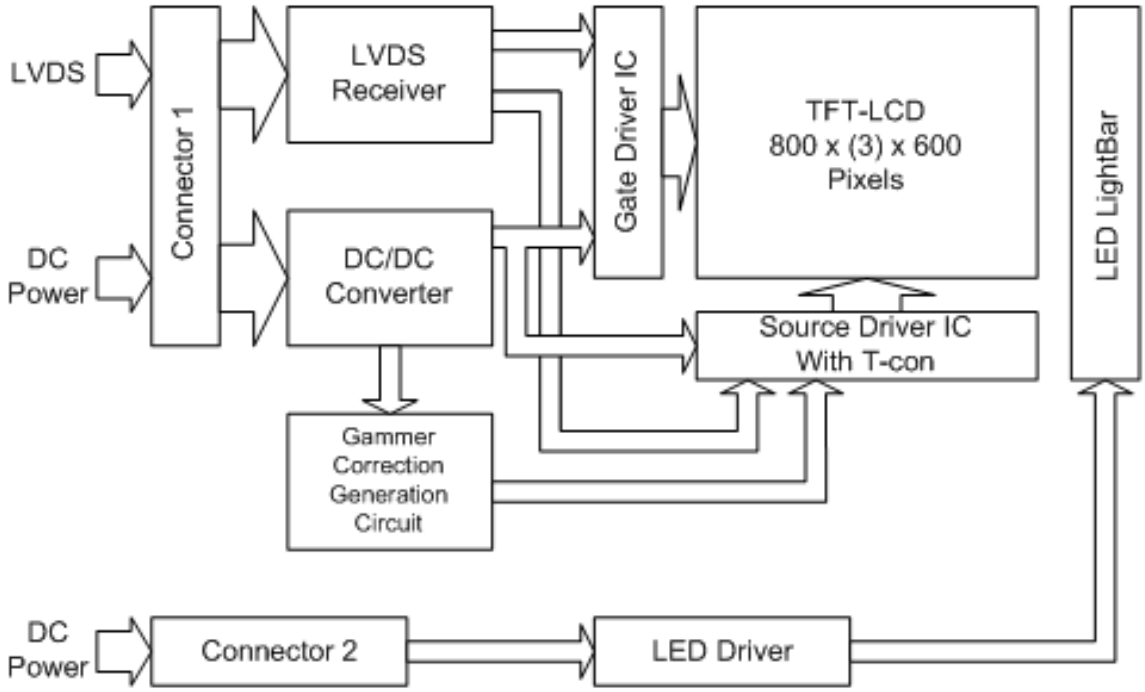
UD : H, LR : H



CN2 : LED Driver Connector

Pin no	Symbol	Function
1	VCC	12V input
2	GND	GND
3	ENABLE	+3.3V:ON, 0V:OFF
4	DIMMING	PWM

7.2 Block Diagram



8. AC Timing characteristic

8.1 AC Timing characteristic of LVDS

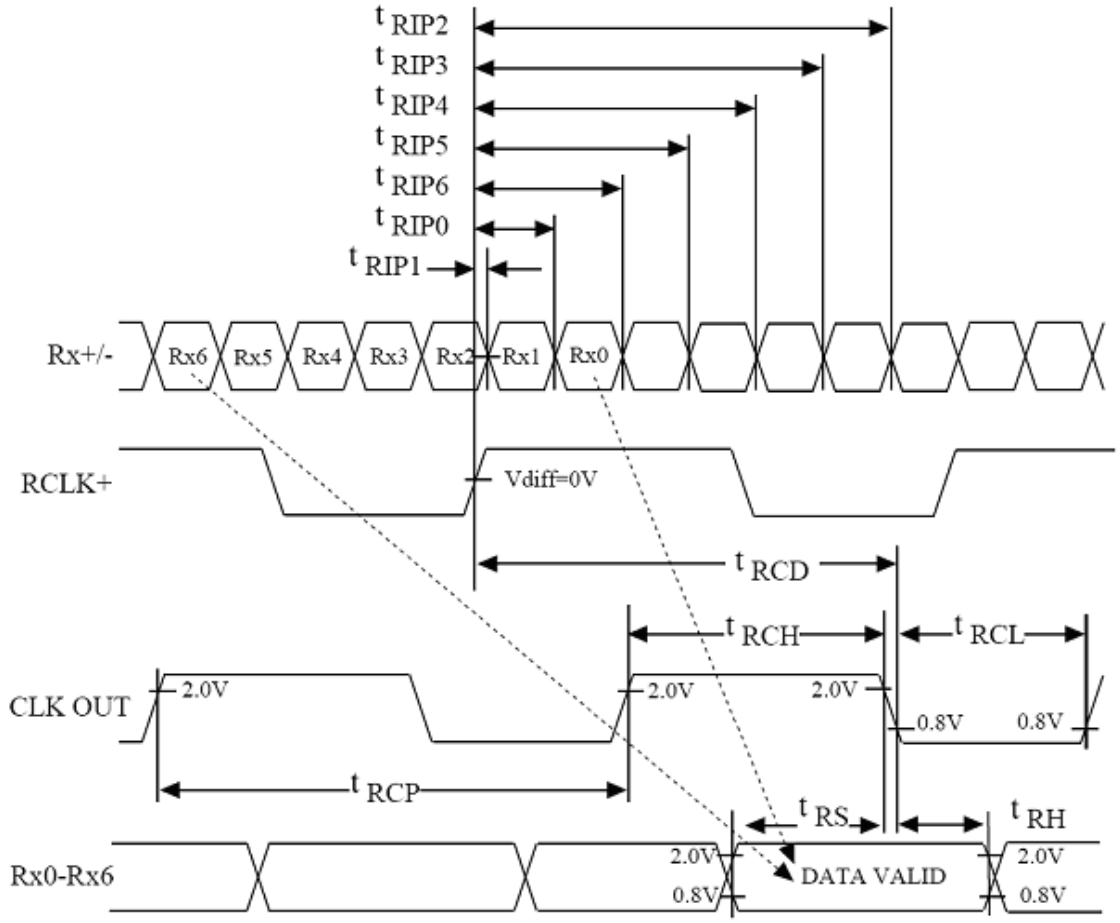
Switching Characteristics

Vcc = 3.0 - 3.6V, Ta = -10 - +70 ˚C

RECEIVER

t_{RCP}	CLK OUT Period	11.76	T	50.0	ns
t_{RCH}	CLK OUT High Time		4T/7		ns
t_{RCL}	CLK OUT Low Time		3T/7		ns
t_{RCD}	RCLK+/- to CLK OUT Delay		5T/7		ns
t_{RS}	TTL Data Setup to CLK OUT	3T/7-2.5			ns
t_{RH}	TTL Data Hold from CLK OUT	4T/7-3.5			ns
t_{TLH}	TTL Low to High Transition Time		3.0	5.0	ns
t_{THL}	TTL High to Low Transition Time		3.0	5.0	ns
t_{RIP1}	Input Data Position 0 (T=11.76ns)	-0.4	0.0	0.4	ns
t_{RIP0}	Input Data Position 1 (T=11.76ns)	T/7-0.4	T/7	T/7+0.4	ns
t_{RIP6}	Input Data Position 2 (T=11.76ns)	2T/7-0.4	2T/7	2T/7+0.4	ns
t_{RIP5}	Input Data Position 3 (T=11.76ns)	3T/7-0.4	3T/7	3T/7+0.4	ns
t_{RIP4}	Input Data Position 4 (T=11.76ns)	4T/7-0.4	4T/7	4T/7+0.4	ns
t_{RIP3}	Input Data Position 5 (T=11.76ns)	5T/7-0.4	5T/7	5T/7+0.4	ns
t_{RIP2}	Input Data Position 6 (T=11.76ns)	6T/7-0.4	6T/7	6T/7+0.4	ns
t_{RPLL}	Phase Lock Loop Set			10.0	ms

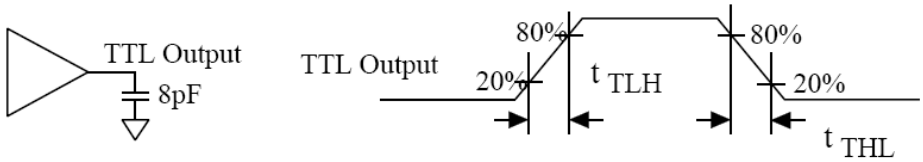
RECEIVER DEVICE



Note:
1) Vdiff = (RA+) - (RA-), ... (RCLK+) - (RCLK-)

RECEIVER DEVICE TRANSITION TIMES

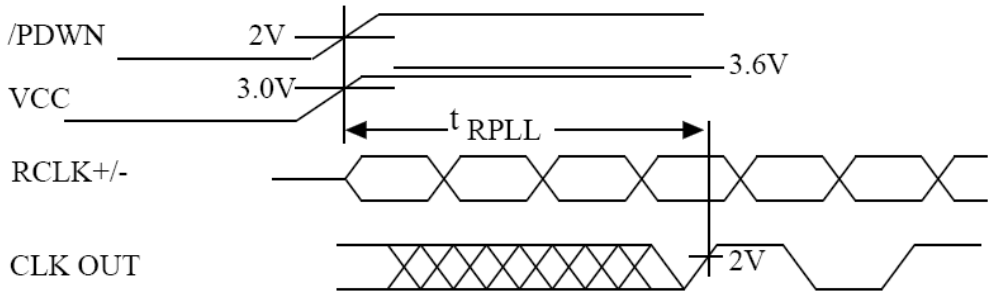
TTL Output



TTL output load

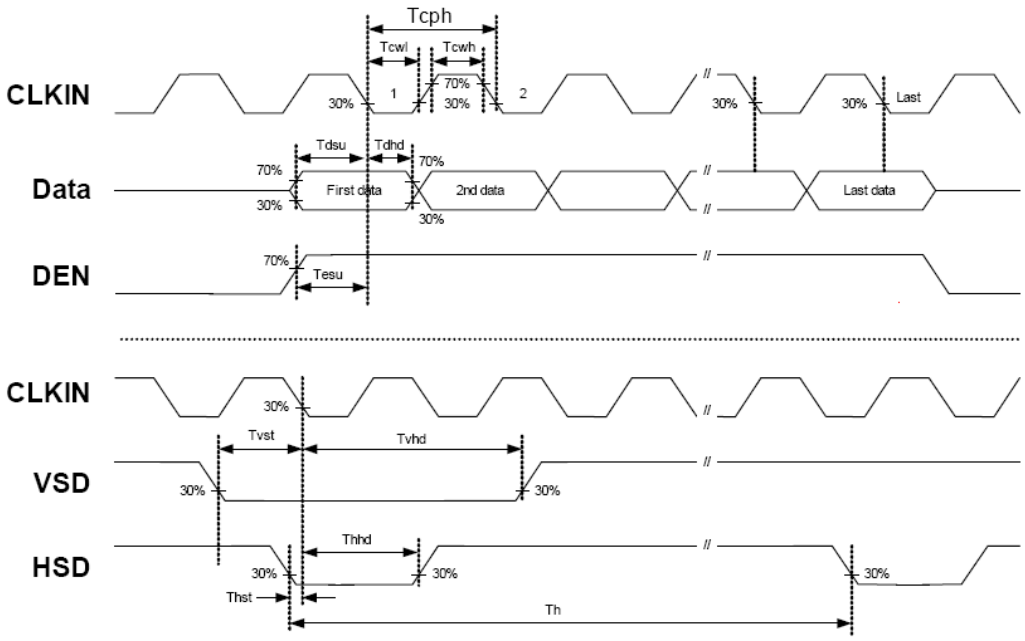
PHASE LOCK LOOP SET TIME

RECEIVER DEVICE

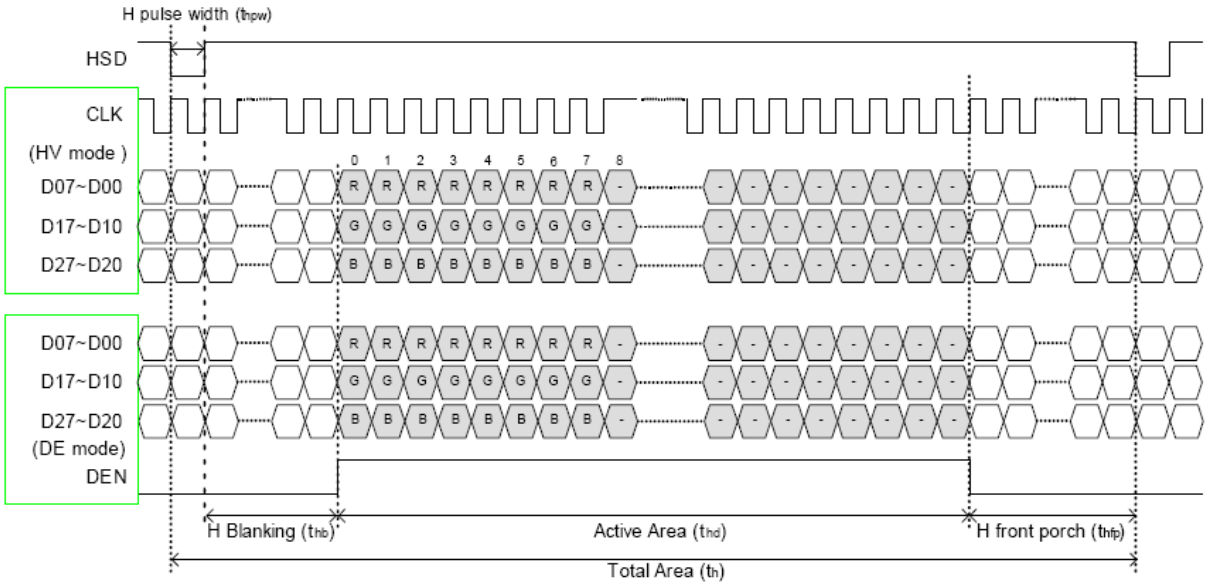


8.2 AC Timing characteristic of Panel

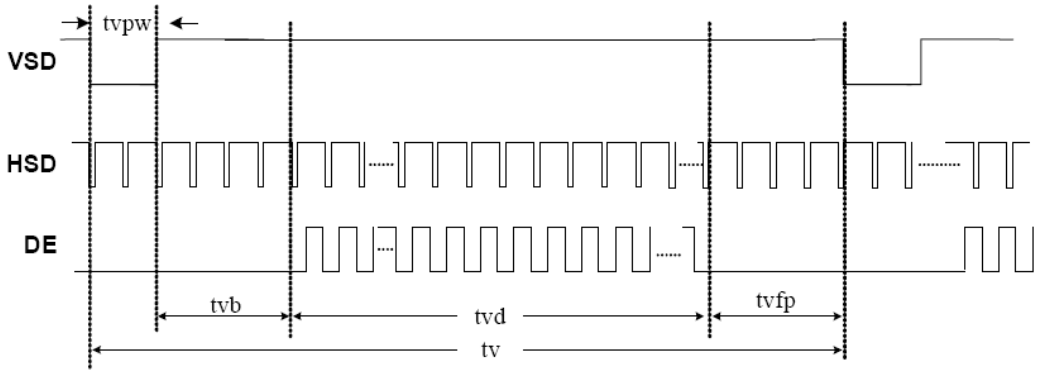
Item	Symbol	Min.	Typ.	Max.	Unit	Note
DCLK cycle time	Tcph	20			ns	
DCLK frequency	fclk		40	50	MHz	
DCLK pulse duty	Tcwh	40	50	60	%	
VSD setup time	Tvst	8			ns	
VSD hold time	Tvhd	8			ns	
HSD setup time	Thst	8			ns	
HSD hold time	Thhd	8			ns	
Data setup time	Tdsu	8			ns	
Data hold time	Tdhd	8			ns	
DE setup time	Tesu	8			ns	
DE hold time	Tehd	8			ns	
Horizontal display area	thd		800		Tcph	
HSD period time	th		1000		Tcph	
HSD pulse width	thpw	1	48		Tcph	
HSD back porch	thb		40		Tcph	
HSD front porch	thfp		112		Tcph	
Vertical display area	tvd		600		th	
VSD period time	tv		660		th	
VSD pulse width	tvpw		3		th	
VSD back porch	tvb		39		th	
VSD front porch	tvfp		18		th	



Sampling clock timing

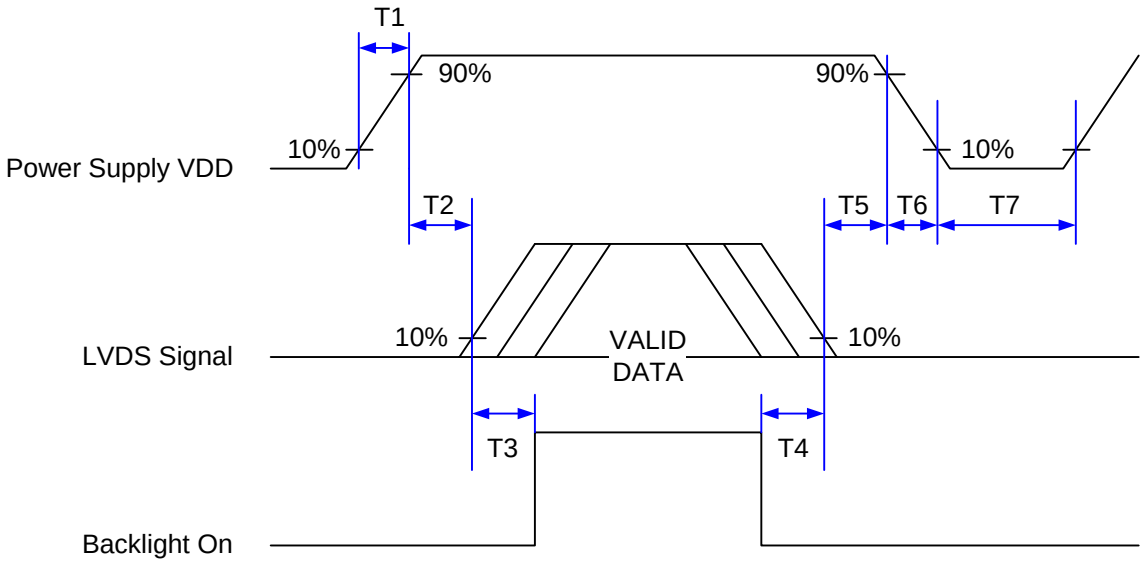


Horizontal display timing range



Vertical timing

8.3 Power ON/OFF Sequence



8.3.1 Power ON/OFF sequence timing

Symbol	Value			Unit
	Min.	Typ.	Max.	
T1	0.5	--	20	ms
T2	0	40	50	ms
T3	200	--	--	ms
T4	200	--	--	ms
T5	0	40	50	ms
T6	0	--	20	ms
T7	1000	--	--	ms

9. Projected capacitive-type Touch panel specification

9.1 Basic Characteristic

Item	Specification
Type	Projective Capacitive Touch Panel
Activation	**Multi-touch**
X/Y Position Reporting	Absolute Position
Touch Force	No contact pressure required
Calibration	No need for calibration
Report Rate	Approx. 80 points/sec
Control IC	**ILI2511**

Electrical Characteristics

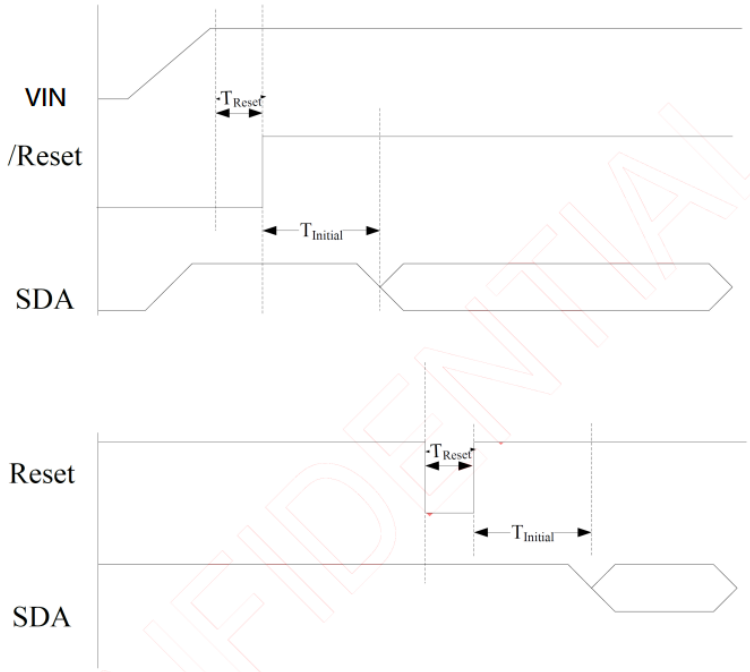
Specify the normal operating condition
(DGND=0V)

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Power supply voltage	VDD	-	3	-	V	

Interface

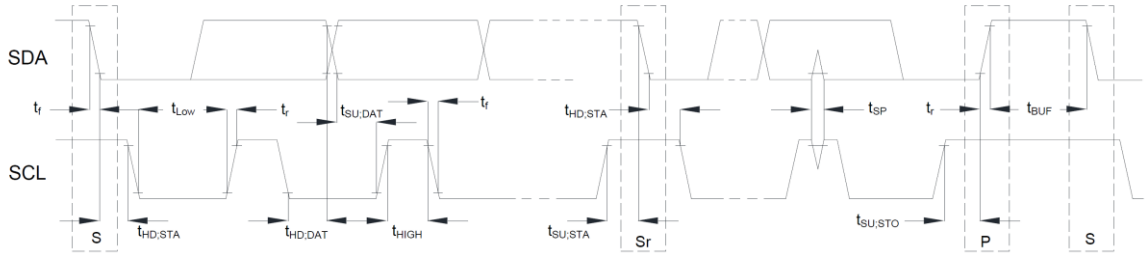
CN6		
Pin No.	**Symbol**	**Function**
1	DGND	Digital ground
2	SDA	I2C Data
3	SCL	I2C Clock
4	VIN	USB Power 3.3V
5	INT	Interrupt pin, active low.
6	RESET	Reset pin, active low.

Power- on Timing Chart



Symbol	Parameter	MIN.	MAX.	Unit
$T_{Initial}$	After powering-on or resetting the device, the device needs I_{nitial} time to configure the system.	-	100	ms
T_{Reset}	/Reset pin low hold time	50	-	μs

I2C AC Waveform



I2C Characteristics

Symbol	Parameter	100KHz			400KHz		
		Min	Max	Unit	Min	Max	Unit
f_{SCL}	SCL clock frequency	0	100	kHz	0	400	KHz
$t_{HD;STA}$	Hold time (repeated) START condition. After this period, the first clock pulse is generated	4.0	–	µs	0.6	–	µs
t_{LOW}	LOW period of the SCL clock	4.7	–	µs	1.3	–	µs
t_{HIGH}	HIGH period of the SCL clock	4.0	–	µs	0.6	–	µs
$t_{SU;STA}$	Set-up time for a repeated START condition	4.7	–	µs	0.6	–	µs
$t_{HD;DAT}$	Data hold time	0	3.45	µs	0	0.9	µs
$t_{SU;DAT}$	Data set-up time	250	–	ns	100	–	ns
t_r	Rise time of both SDA and SCL signals	–	1000	ns	–	300	ns
t_f	Fall time of both SDA and SCL signals	–	300	ns	–	300	ns
$t_{SU;STO}$	Set-up time for STOP condition	4.0	–	µs	0.6	–	µs
t_{BUF}	Bus free time between a STOP and START condition	4.7	–	µs	1.3	–	µs

Device Address

MSB							LSB
1	0	0	0	0	0	1	0/1
Device Address							R/W

7-bit Device Address: 0x41

8-bit Device Read Address:0x83

8-bit Device Write Address:0x82

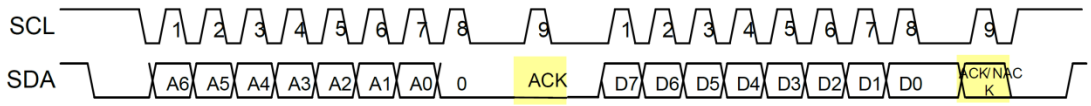
Data Transfer

Data is transferred over the I2C bus with 8-bit address and 8-bit data.

1	7	1	1	8	1	1
S	Slave Address	Wr	A	Data Byte	A	P

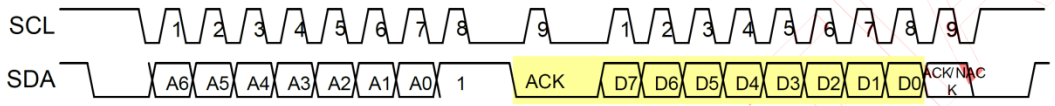
S	Start Condition
Sr	Repeated Start Condition
Rd	Read (bit value of 1)
Wr	Write (bit value of 0)
A/NA	Acknowledge (this bit position may be '0' for an ACK or '1' for a NACK)
P	Stop Condition
	Master-to-Slave
	Slave-to-Master
---	Continue

I2C Write timing



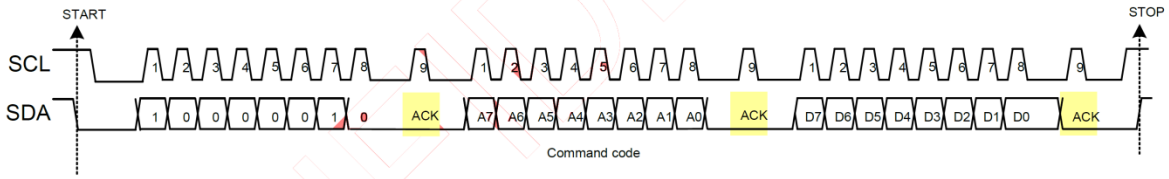
=> slave to master

I2C Read timing



=> slave to master

Byte Write



S	Slave Address	Wr	A	Command Code	A	Data Byte	A	P

F Byte Write

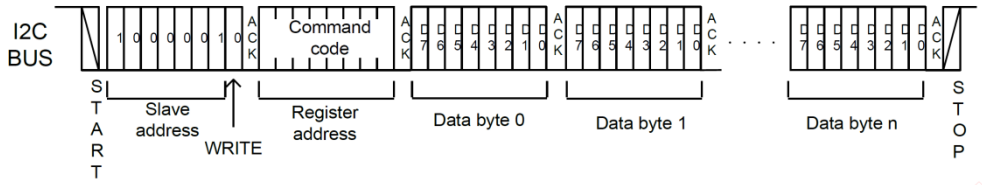
Byte Read

c

S	Slave Address	Wr	A	Command Code	A	Sr	Slave Address	Rd	A	Data Byte	A	P

F Byte Read

Multi-Byte Write

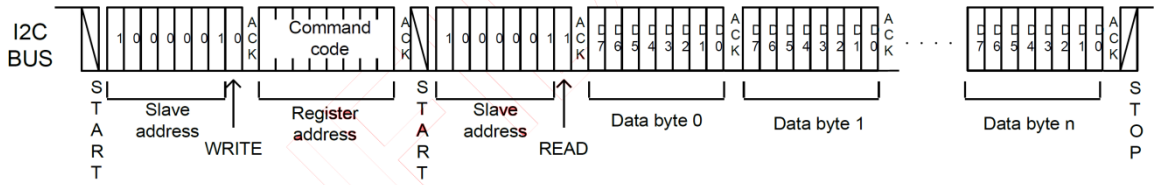


S	Slave Address	Wr	A	Command Code	A	Data Byte 0	A	Data Byte 1	A

Data Byte 2	A	-----	A	Data Byte N	A	P

F Multi-Byte Write

Multi-Byte Read



S	Slave Address	Wr	A	Command Code	A	Sr	Slave Address	Rd	A	Data Byte 0	A

Data Byte 1	A	-----	A	Data Byte N	NA	P

F Multi-Byte Read

Format Protocol

Protocol V3.X Command List

CMD Code	Name	Set /Get	Note	b7	b6	b5	b4	b3	b2	b1	b0
0x10	Touch Information	Get		0: No touch 1: Last Report at ID 0 to ID 5 (include release status) 2: Last Report at ID 6 to ID 9 (include release status)							
			ID0	1: Touch Down, 0: Touch Off	0	X_High direction coordinate					
				X_Low direction coordinate							
				0	0	Y_High direction coordinate					
				Y_Low direction coordinate							
				Touch Pressure							
			ID1	1: Touch Down, 0: Touch Off	0	X_High direction coordinate					
				X_Low direction coordinate							
				0	0	Y_High direction coordinate					
				Y_Low direction coordinate							
				Touch Pressure							
			ID2	1: Touch Down, 0: Touch Off	0	X_High direction coordinate					
				X_Low direction coordinate							
				0	0	Y_High direction coordinate					
				Y_Low direction coordinate							
				Touch Pressure							
			ID3	1: Touch Down, 0: Touch Off	0	X_High direction coordinate					
				X_Low direction coordinate							
				0	0	Y_High direction coordinate					
				Y_Low direction coordinate							
				Touch Pressure							
			ID4	1: Touch Down, 0: Touch Off	0	X_High direction coordinate					
				X_Low direction coordinate							
				0	0	Y_High direction coordinate					
				Y_Low direction coordinate							
				Touch Pressure							

			ID5	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
0x14	Touch Information 2	Get	ID6	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID7	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID8	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID9	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
0x20				The maximum X coordinate (bit 7:0)		
				The maximum X coordinate (bit 15:8)		
				The maximum Y coordinate (bit 7:0)		
				The maximum Y coordinate (bit 15:8)		
				The channel numbers of X direction		
				The channel numbers of Y direction		
				The maximum report points		

				The channel numbers of TouchKey / Scrolling Bar
				For Touch Key Application (Maximum supports 31 Touch Key) Byte 8 : The Touch Key number (<32) Byte 9: 0xFF
0x30	Enter Sleep Mode	Set		--
0x40	Firmware Version	Get		Chip ID Code
				Major firmware version
				Minor firmware version
				Release firmware version
				For Customer Firmware Version
				For Customer Firmware Version
				For Customer Firmware Version
				For Customer Firmware Version
0x42		Get		Major protocol version : 0x03
				Minor protocol version : XX
				Release protocol version : XX

Protocol V3.X Data Format

CMD Code	Name	Set / Get	Note	b7	b6	b5	b4	b3	b2	b1	b0
0x10	Touch Information	Get	Packet Number	0: No touch 1: Last Report at ID 0 to ID 5 (include release status) 2: Last Report at ID 6 to ID 9 (include release status)							
			ID0	1: Touch Down, 0: Touch Off	0	X_High direction coordinate					
				X_Low direction coordinate							
				0	0	Y_High direction coordinate					
				Y_Low direction coordinate							
				Touch Pressure							

			ID1	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID2	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID3	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID4	1: Touch Down, 0: Touch Off	0	X_High direction coordinate

				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID5	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		

10. RELIABILITY TEST CONTITIONS

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=240 hrs	
Low Temperature Operation	-20±3°C , t=240 hrs	
High Temperature Storage	80±3°C , t=240 hrs	1,2
Low Temperature Storage	-30±3°C , t=240 hrs	1,2
Storage at High Temperature and Humidity	60°C, 90% RH , 240 hrs	1,2
Thermal Shock Test	-20°C (30min) ~ 70°C (30min) 100 cycles	1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions (15-35°C , 45-65%RH).

Note 3 : The module shouldn't be tested more than one condition, and all the test conditions are independent.

Note 4 : All the reliability tests should be done without protective film on the module.

Definitions of life end point:

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of the initial value.

11. USE PRECAUTIONS

11.1 Handling precautions

1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

11.2 Installing precautions

1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. 1MΩ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

11.3 Storage precautions

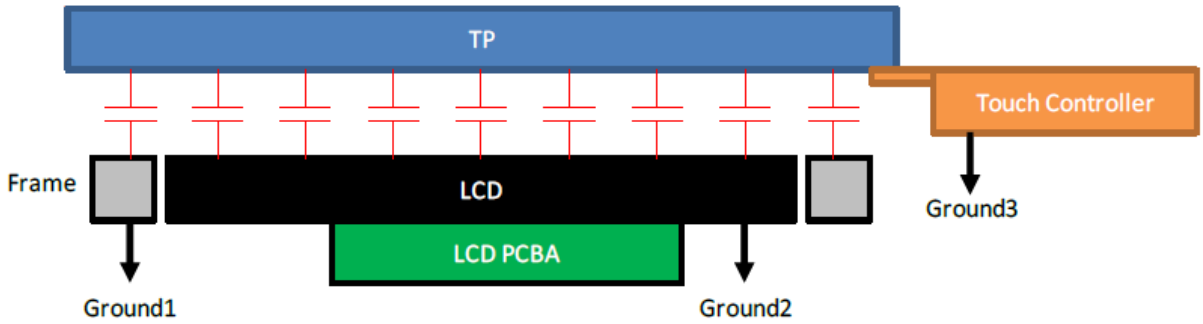
1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.
3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

11.4 Operating precautions

1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC dive voltage.
4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
5) Make certain that each signal noise level is within the standard (L level: 0.2Vdd or less and H level: 0.8Vdd or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.
8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

11.5 Other

1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
2) Do not keep the LCD at the same display pattern continually. The residual image will happen and it will damage the LCD. Please use screen saver.
3) TP needs to work in environment with stable stray capacitance. In order to minimize the variation in stray capacitance, all conductive mechanical parts must not be floating. Intermittent floating any conductive part around the touch sensor may cause significant stray capacitance change and abnormal touch function. It is recommended to keep all conductive parts having same electrical potential as the GND of the touch controller module.

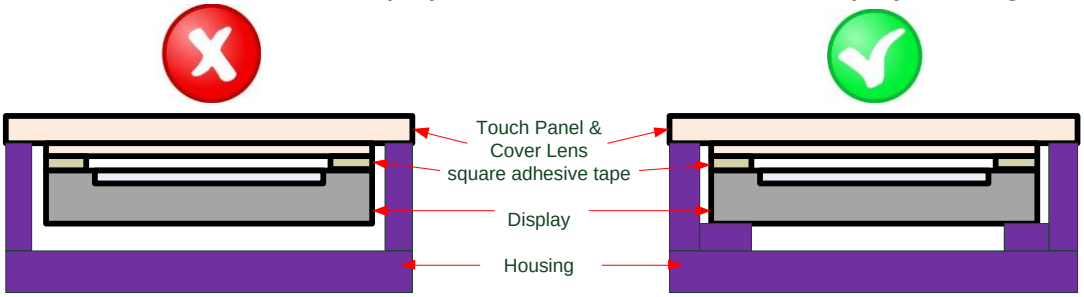


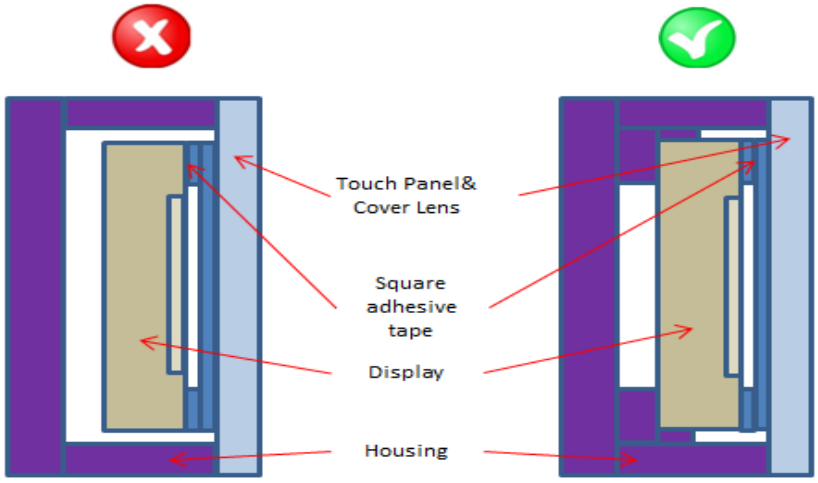
GND1, GND2 and GND3 should be connected together to have the same ground

4) AMIPRE will provide one year warranty for all products and three months warrantee for all repairing products.

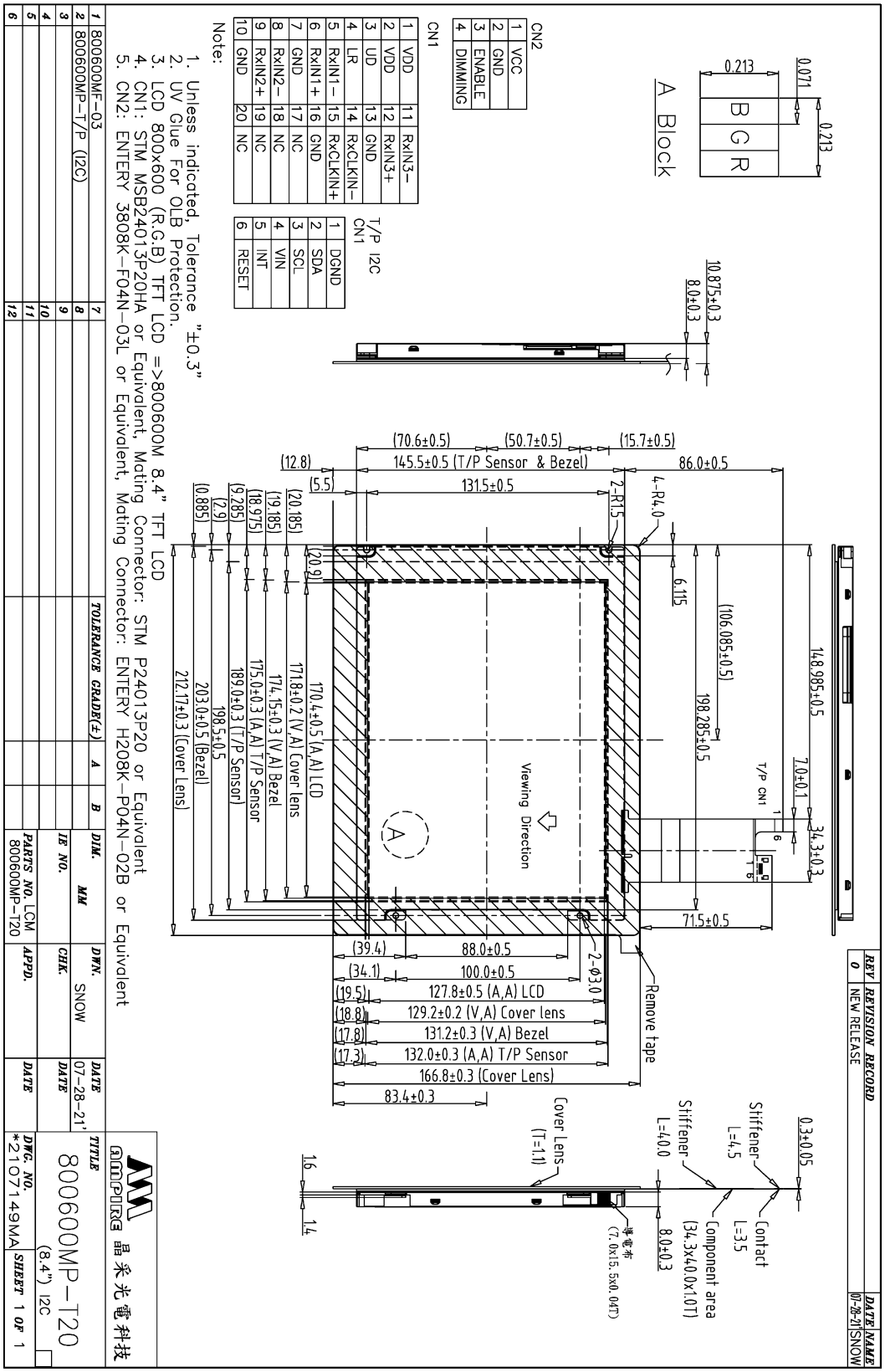
11.6 Mechanism

1) Please mount LCD module by using mounting holes arranged in four corners tightly.
2) The square adhesive tape which is between the touch panel and display can't provide well supporting in the long term and high ambient temperature condition. Whether upright or horizontal position the support holder which is in the back side of the display is needed. Do not let the display floating.





12. OUTLINE DIMENSION



REV	REVISION RECORD	DATE	NAME
0	NEW RELEASE	07-28-21	SNOW

T/P I2C
CN1

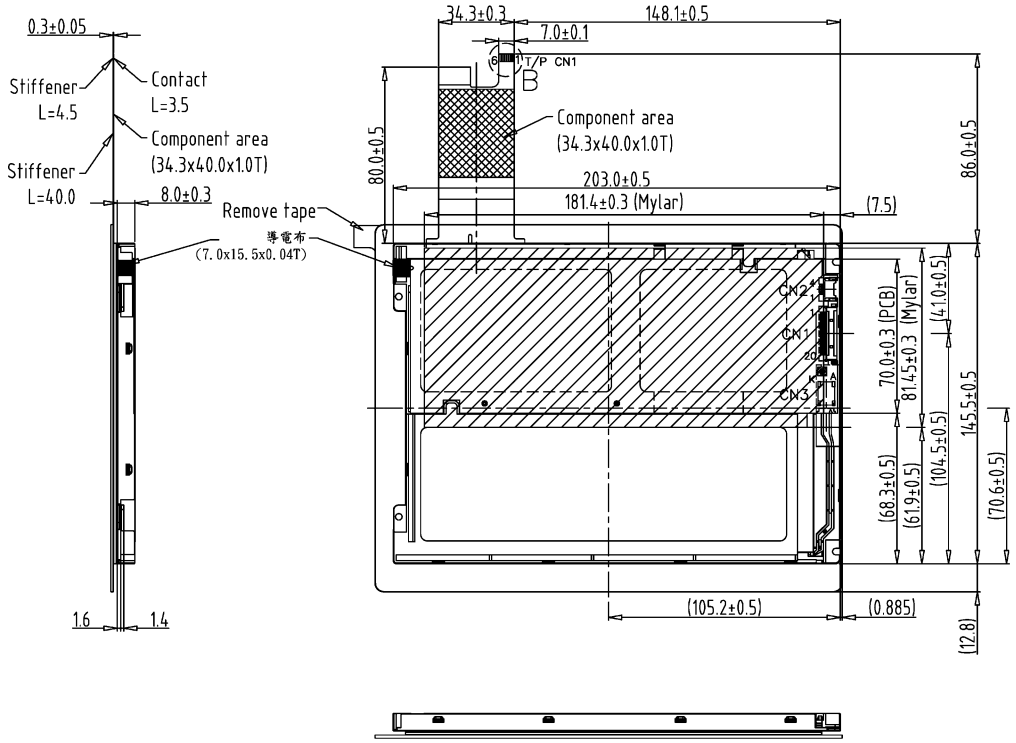
1	DGND
2	SDA
3	SCL
4	VIN
5	INT
6	RESET

CN2

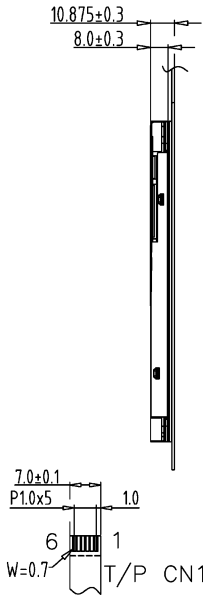
1	VCC
2	GND
3	ENABLE
4	DIMMING

CN1

1	VDD	11	RxIN3−
2	VDD	12	RxIN3+
3	UD	13	GND
4	LR	14	RxCLKIN−
5	RxIN1−	15	RxCLKIN+
6	RxIN1+	16	GND
7	GND	17	NC
8	RxIN2−	18	NC
9	RxIN2+	19	NC
10	GND	20	NC



Back View



B Block

Note:

1. Unless indicated, Tolerance "±0.3"
2. UV Glue For OLB Protection.
3. LCD 800x600 (R.G.B) TFT LCD =>800600M 8.4" TFT LCD
4. CN1: STM MSB24013P20HA or Equivalent, Mating Connector: STM P24013P20 or Equivalent
5. CN2: ENTERY 3808K−F04N−03L or Equivalent, Mating Connector: ENTERY H208K−P04N−02B or Equivalent

1	800600MF−03	7		TOLERANCE GRADE(±)	A	B	DIM. MM	DWN. SNOW	DATE 07−28−21	TITLE 800600MP−T20 (8.4") I2C
2	800600MP−T/P (I2C)	8					IE NO.	CHK.	DATE	
3		9								
4		10								
5		11					PARTS NO. LCM−1 800600MP−T20	APPD.	DATE	DWG. NO. *2107150MA
6		12								SHEET 1 OF 1

AMPIRE 晶采光電科技

AMPIRE CO., LTD.

Date : 2021/08/09